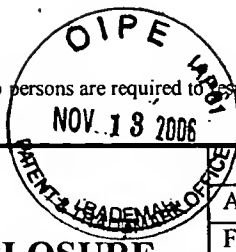


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Substitute for Form 1449/PTO

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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## **Complete if Known**

Application Number	10/572,655
Filing Date	March 20, 2006
First Named Inventor	Dominik EISERT
Art Unit	-- 2811
Examiner Name	Cathy N. Lam
Attorney Docket Number	5367-223PUS

Sheet 1 of 3

## **U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind-Code <sup>2</sup> (if known)			
/C.L./		US-2003/0127654 A1	07/10/2003	Eisert et al.	
		US-6,649,939 B1	11/18/2003	Wirth	
		US-6,111,272	08/29/2000	Heinen	
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		US-5,200,668	04/06/1993	Ohashi et al.	

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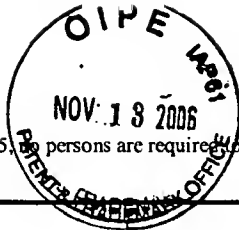
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09/30/2008

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Sheet 2 of 3	Attorney Docket Number	5367-223PUS	

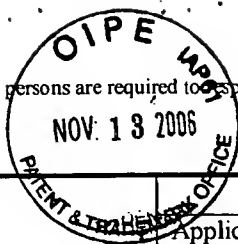
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>2</sup> Number <sup>4</sup> Kind-Code <sup>5</sup> (if known)				
/C.L./		WO 02/41406 A1	05/23/2002	Eliashevich et al.		
		JP 2001-168387	06/22/2001	Shibata et al.		See English Abstract
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		WO 96/37000	11/21/1996	Heinen		See English Abstract
		WO 96/26550	08/29/1996	Fischer et al.		See English Abstract
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		DE 100 06 738 A1	09/13/2001	Eisert et al.		

Examiner Signature	/Cathy Lam/	Date Considered	09/30/2008
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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/C.L./		J. Cao et al., "Improved quality GaN by growth on compliant silicon-on-insulator substrates using metalorganic chemical vapor deposition", Journal of Applied Physics, Vol. 83, No. 7, pp. 3829-3834, April 1, 1998	
		R. Windisch et al., "High-Efficiency Surface-Textured LEDs", Compound Semiconductor, Vol. 6, No. 4, pp. 55-58, May/June 2000	
		Y.K. Song et al., "Resonant-cavity InGa <sub>N</sub> quantum-well blue light-emitting diodes", Applied Physics Letters, Vol. 77, No. 12, pp. 1744-1746, September 18, 2000	
		A.J. Steckl et al., "Growth and Characterization of GaN Thin Films on SiC SOI Substrates", Journal of Electronics Materials, Vol. 26, No. 3, pp. 217-223, 1997	
		W.S. Wong et al., "Integration of GaN Thin Films with Dissimilar Substrate Materials by Pd-In Metal Bonding and Laser Lift-Off", Journal of Electronic Materials, Vol. 28, No. 12, pp. 1409-1413, 1999	
		I. Schnitzer et al., "30%, external quantum efficiency from surface textured, thin-film light-emitting diodes", Applied Physics Letter, Vol. 63, No. 18, pp. 2174-2176, October 18, 1993	

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